



CMOS High Efficiency On-chip Power Management (Analog Circuits and Signal Processing)

By John Hu, Mohammed Ismail

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This book will introduce various power management integrated circuits (IC) design techniques to build future energy-efficient “green” electronics. The goal is to achieve high efficiency, which is essential to meet consumers’ growing need for longer battery lives. The focus is to study topologies amiable for full on-chip implementation (few external components) in the mainstream CMOS technology, which will reduce the physical size and the manufacturing cost of the devices.

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Editorial Review

From the Back Cover

This book deals with the subject matter of power management integrated circuit (IC) design, or integrated power electronics, as a response to the growing need for energy-efficient electronics. The authors introduce various power management IC design techniques to build future energy-efficient “green” electronics. The goal is to achieve high efficiency, which is essential to meet consumers’ growing need for longer battery lives. The focus is to study topologies amiable for full on-chip implementation (few external components) in the mainstream CMOS technology, which will reduce the physical size and the manufacturing cost of the devices.

- Describes a number of techniques at circuits and systems level that increase sleep-mode efficiency to prolong the battery life, without sacrificing performance parameters;
- Enables readers to design for compactness, which requires fewer bulky external components and circuit topologies that lend themselves easily to full on-chip integration;
- Offers insights on how the efficiency boosting techniques for power management IC designs work toward society’s quest for higher energy efficiency.

Users Review

From reader reviews:

Robert Young:

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