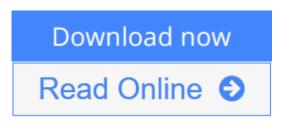# Microcontrollers: From Assembly Language to C Using the PIC24 Family

By Robert B. Reese, J. W. Bruce, Bryan A. Jones

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This completely updated second edition of MICROCONTROLLERS: FROM ASSEMBLY LANGUAGE TO C USING THE PIC24 FAMILY covers assembly language, C programming, and hardware interfacing for the Microchip PIC24 family, a recently updated microcontroller family from Microchip. Hardware interfacing topics include parallel port usage, analog-to-digital conversion, digital-to-analog conversion, the serial peripheral bus (SPI), the inter-integrated circuit bus (I2C), asynchronous serial communication, and timers. Assembly language programming is covered in the context of the PIC24 instruction set, and no initial knowledge of assembly language programming is assumed. Specific hardware interfacing topics covered are parallel IO, analog-to-digital/digital-to-analog conversion, pulse width modulation, timer usage for IO polling, and industry standard serial interface standards. Interfacing examples include external devices such as pushbutton switches, LEDs, serial EEPROMs, liquid crystal displays (LCDs), keypads, rotary encoders, external digital-to-analog converters, DC motors, servos, temperature sensors, and IR receivers. Master the PIC24 family with MICROCONTROLLERS: FROM ASSEMBLY LANGUAGE TO C USING THE PIC24 FAMILY.

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Editorial Review

About the Author
Robert B. Reese received the B.S. degree from Louisiana Tech University, Ruston, in 1979 and the M.S. and Ph.D. degrees from Texas A&M University, College Station, in 1982 and 1985, respectively, all in electrical engineering. He served as a Member of the Technical Staff of the Microelectronics and Computer Technology Corporation (MCC), Austin, TX, from 1985 to 1988. Since 1988, he has been with the Department of Electrical and Computer Engineering at Mississippi State University, Mississippi State, where he is an Associate Professor. Courses that he teaches include Microprocessors, VLSI systems, Digital System design, and senior design. His research interests include self-timed digital systems and computer architecture.

J.W. Bruce received the B.S.E. from the University of Alabama in Huntsville in 1991, the M.S.E.E. from the Georgia Institute of Technology in 1993, and the Ph.D. from the University of Nevada Las Vegas in 2000, all in electrical engineering. Dr. Bruce has served as a member of the technical staff at the Mevatec Corporation providing engineering support to the Marshall Space Flight Center Microgravity Research Program. He also worked in the 3D Workstation Graphics Group at the Integraph Corporation designing the world's first OpenGL graphics accelerator for the Windows operating system. Since 2000, Dr. Bruce has served in the Department of Electrical and Computer Engineering at Mississippi State University. Dr. Bruce has contributed to the research areas of data converter architecture design and embedded systems design. His research has resulted in more than 30 technical publications and one book chapter.

Bryan A. Jones received the B.S.E.E. and M.S. degrees in electrical engineer-ing from Rice University, Houston, TX, in 1995 and 2002, respectively, and the Ph.D. degree in electrical engineering from Clemson University, Clemson, SC, in 2005. From 1996 to 2000, he was a Hardware Design Engineer for Compaq, specializing in board layout for high-availability RAID controllers. He is currently an Assistant Professor at Mississippi State University, Mississippi State, MS. His research interests include micro air vehicles, robotics, real-time control-system implementation, rapid prototyping for real-time systems, and modeling and analysis of mechatronic systems.

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